



# CardBus System Architecture

*MindShare Inc., Don Anderson, Tom Shanley*

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"The 'must-have' PC architecture reference set." -PC Magazine's "Read Only" column CardBus System Architecture describes 32-bit PC Cards, called CardBus PC Cards, and the hardware and software interfaces required to implement the new CardBus solution. This book focuses on the design and implementation of CardBus Cards and the host systems required to support them, including relationships and interaction between hardware and software elements associated with CardBus Cards and their host systems. Developed by the Personal Computer Memory Card International Association (PCMCIA) and the Japan Electronics Industry Development Association (JEIDA), the PC Card standard defines both 16-bit PC Cards and CardBus Cards. The PC Card Standard weaves two previous industry standards together--the 16-bit PC Card Standard and the Peripheral Component Interconnect (PCI)--to form CardBus. For background information see PCMCIA System Architecture, Second Edition: 16-Bit PC Cards and PCI System Architecture , Third Edition (both Addison-Wesley, 1995). CardBus System Architecture includes the following topics: \*CardBus socket interface \*supporting 16-bit and CardBus PC Cards \*determining card type and initial Vcc requirements \*CardBus signaling environment \*CardBus bridge requirements \*bridge configuration header definition \*bridge configuration \*types of CardBus transfers \*CardBus configuration header \*card information structure (CIS) \*enabling and configuring CardBus Cards \*interrupt routing and handling \*legacy DMA support \*PC Card event notification and handling \*card and socket services. If you design or test software or hardware that involves 32-bit PC Cards, CardBus System Architecture is an essential, time-saving tool. The PC System Architecture Series is a crisply written and comprehensive set of guides to the most important PC hardware standards. Each title explains from a programmer's perspective the architecture, features, and operations of systems built using one particular type of chip or hardware specification.

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